

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1697652
<b>Application Number:</b>	10060750
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	7058
<b>Title of Invention:</b>	System for controlling external models used for verification of system on a chip (SOC) interfaces
<b>First Named Inventor/Applicant Name:</b>	Robert J. Devins
<b>Customer Number:</b>	29154
<b>Filer:</b>	Frederick W. Gibb III/Tylene McCoy
<b>Filer Authorized By:</b>	Frederick W. Gibb III
<b>Attorney Docket Number:</b>	BUR9-2001-0016-US1
<b>Receipt Date:</b>	19-APR-2007
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<b>Application Type:</b>	Utility

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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Appeal Brief Filed	BUR920010016US1_Appeal Brief.pdf	242932	no	59

### Warnings:

Information:		
<table border="1"><tr><td>Total Files Size (in bytes):</td><td>242932</td></tr></table>	Total Files Size (in bytes):	242932
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